



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PTO/SB/21 (03-03)

Approved for use through 04/30/2003. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application Number	10/723,547
		Filing Date	11/26/2003
		First Named Inventor	Samir Chaudhry
		Art Unit	2811
		Examiner Name	Unassigned
Total Number of Pages in This Submission		Attorney Docket Number	Chaudhry 26-19-9-13-6/075903-285

### ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to a Technology Center (TC)
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	1. Postcard;
<input checked="" type="checkbox"/> Supplemental Information Disclosure Statement	<input type="checkbox"/> Request for Refund	2. Referenced articles.
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
Remarks		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual	John L. DeAngelis, Jr., Esquire, Reg. No. 30,622 Reutisse Brownlee Wolter Mora & Maire, P.A.
Signature	
Date	March 5, 2004

### CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:

03/ 5 /2004

Typed or printed	Pamela A. Pagel
Signature	
Date	03/ 5 /2004

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Samir Chaudhry, et al

Group Art Unit: 2811

Serial No.: 10/723,547

Examiner: Unassigned

Filed: 11/26/2003

Confirmation No.: 9745

Atty. Docket No.: Chaudhry 26-19-9-13-6/075903-285

For: A Vertical Replacement-Gate Junction Field-Effect Transistor

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

- 1. Pursuant to 37 CFR 1.97(b)  
(within 3 months of filing or prior to 1<sup>st</sup> Office Action)
- 2. Certification Pursuant to 37 CFR 1.97(c)  
(before Final Office Action or Allowance)
- 3. Fee Payment Pursuant to 37 CFR 1.97(c)  
(before Final Office Action or Allowance)
- 4. Petition, Certification & Petition Fee Payment Pursuant to 37 CFR 1.97(d) (before issue fee payment)

March 5, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following are submitted in the above identified application in compliance with 37 CFR 1.97 and 1.98:

- 5. A list of documents on form PTO-1449 together with copies of each identified document and a translation or a concise explanation of each non-English language document is enclosed herewith.

This paper is submitted in accordance with:

- 6. 37 CFR 1.97(b): [within 3 months of filing or prior to 1<sup>st</sup> Office Action];
- 7. 37 CFR 1.97(c): [before Final Office Action or Allowance, whichever is earlier; and]
- 8. The required certification made in item 11 below; or

[ ] 9. The \$240.00 fee specified in 37 CFR §1.17(p) for submission of this Information Disclosure Statement is authorized in item 14 below.

[ ] 10. 37 CFR § 1.95(d): [before issue fee payment]; and

a) This is a petition for consideration of the subject Information Disclosure Statement. The petition fee (\$130.00) required by 37 CFR 1.17(i)(1) is enclosed via check. (Direct this letter to: "Attention PETITIONS EXAMINER" and if applicable include batch locator information: e.g. "Allowed Files, Batch \_\_\_, Date of Allowance \_\_\_"), and

b) The required Certification is stated in item 11 below.

[ ] 11. Certification

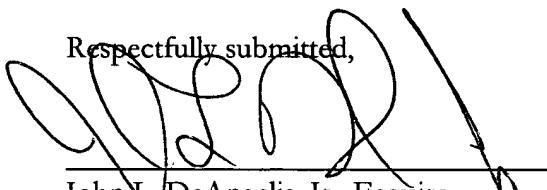
[ ] 12. Each item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or

[ ] 13. No item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing this document after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56(c) more than three (3) months prior to the filing of this Statement.

[ ] 14. Please charge all applicable fees associated with the submittal of this Information Disclosure Statement to Deposit Account No. \_\_\_\_\_.

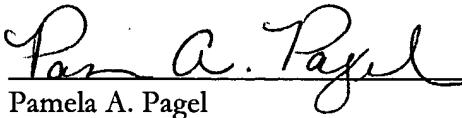
An original and two (2) copies of this document are enclosed.

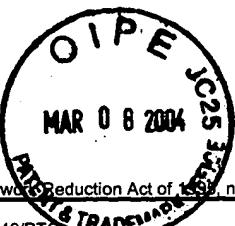
Respectfully submitted,

  
John L. DeAngelis, Jr., Esquire  
Reg. No. 30,622  
Beusse Brownlee Bowdoin & Wolter, P.A.  
390 N. Orange Ave., Suite 2500  
Orlando, FL 32801  
(407) 926-7710

CERTIFICATE OF MAILING

I HEREBY CERTIFY that this Supplemental Information Disclosure Statement is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 5 day of March, 2004.

  
Pamela A. Pagel



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/723,547		
Filing Date	11/26/2003		
First Named Inventor	Samir Chaudhry		
Art Unit	2811		
Examiner Name	Unassigned		
Sheet	1	of	1
		Attorney Docket Number	Chaudhry 26-19-9-13-6/075903-285

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	DUDEK, ET AL, "Lithography-Independent Nanometer Silicon MOSFET's on Insulator", IEEE Transactions on Electron Devices, Vol. 43, No. 10, October 1996, pp. 1626-1631.	
	2	RISCH, ET AL, "Vertical MOS Transistors with 70 nm Channel Length", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1495-1498.	
	3	TAKATO, ET AL, "Impact of Surrounding Gate Transistor (SGT) for Ultra-High-Density LSI's", IEEE Transactions on Electron Devices, Vol. 38, No. 3, March 1991, pp. 573-577.	
	4	TAKATO, ET AL, "High Performance CMOS Surrounding Gate Transistor (SGT) for Ultra High Density LSIs", IEDM 1988, pp. 222-225.	
	5	HERGENROTHER, ET AL, "The Vertical Replacement-Gate (VRG) MOSFET: A 50-nm Vertical MOSFET with Lithography-Independent Gate Length", Technical Digest of IEDM, 1999, pp. 75-78.	
	6	OH, ET AL, "50 nm Vertical Replacement-Gate (VRG) pMOSFETs", IEEE 2000.	
	7	HERGENROTHER, ET AL, "The Vertical Replacement-Gate (VRG) MOSFET: A High Performance Vertical MOSFET with Lithography-Independent Critical Dimensions", no publication information apparent from document.	
	8	MONROE, ET AL, "The Vertical Replacement-Gate (VRG) Process for Scalable, General-purpose Complementary Logic", Paper 7.5, pp. 1-7, date and publication information unknown.	

Examiner Signature	Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.